

REMARKS**I. Introduction**

In response to the Final Office Action dated November 27, 2007, Applicants have amended claims 1, 11, 13, and 22 to more particularly point out and distinctly claim the subject matter of the invention. No new matter has been added.

Applicants note with appreciation the indication that claims 11, 12, 22, and 23 would be allowable if rewritten in independent format. In view of the foregoing amendments and the following remarks, Applicants respectfully submit that all pending claims are in condition for allowance.

II. Claim Rejections Under 35 U.S.C. § 103

Claims 1 – 7, 10, 13 – 18, and 21 stand rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over Moon (USP 7,106,292). Applicants traverse these rejections for at least the following reasons.

Claim 1 recites, among other things, a display comprising a plurality of stages of shift register circuits and a plurality of stages of first dummy shift register circuits, wherein said shift register circuits and said first dummy shift register circuits include a first circuit section having a first transistor of first conductivity type whose drain/source is connected to a first potential, a second transistor of first conductivity type whose drain/source is connected to the source/drain of said first transistor and whose source/drain is connected to a second potential, and a third transistor of first conductivity type whose drain/source is connected to a gate of said first transistor and whose source/drain is connected to said second potential for tuning off said first

transistor when said second transistor is in on state. Claim 13 includes similar recitations. Moon fails to disclose or suggest at least this combination of features.

The Examiner equates transistor M2, depicted in Figure 18 of Moon, with the first transistor recited in the pending claims, transistor M1 with the second transistor, and transistor M6 with the third transistor. The Examiner further equates the VON (Moon, Fig. 18) with the claimed first potential and VOFF with the second potential. In Fig. 18 of Moon, the source of M2 (first transistor) is connected to VOFF (a first potential) and the source of M1 (second transistor) is connected to the drain of M2 (first transistor). However, the drain of M1 (second transistor) is not connected to VON (second potential). Rather, the drain of M1 is connected to clock CK. Thus, Moon fails to disclose or suggest every feature of pending claims 1 and 13.

Under Federal Circuit guidelines, a dependent claim is nonobvious if the independent claim upon which it depends is allowable because all the limitations of the independent claim are contained in the dependent claims, *Harness International Inc. v. Simplimatic Engineering Co.*, 819 F.2d at 1100, 1108 (Fed. Cir. 1987). Accordingly, as independent claims 1 and 13 are patentable for at least the reasons set forth above, it is respectfully submitted that all dependent claims are also in condition for allowance. In addition, it is respectfully submitted that the dependent claims are patentable based on their own merits by adding novel and non-obvious features to the combination.

III. Conclusion

Accordingly, it is urged that the application, as now amended, is in condition for allowance, an indication of which is respectfully solicited. If there are any outstanding issues

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that might be resolved by an interview or an Examiner's amendment, Examiner is requested to call Applicant's attorney at the telephone number shown below.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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